**Amendments to the Claims:**

This listing of claims will replace all prior versions and listings of claims in the application:

**Listing of the Claims:**

1. (Currently Amended) An electronic device comprising:

a housing to ~~enable~~ have the device ~~to be~~ docked into a notebook computer having a memory to store a first operating system ~~and exclusive of a central processing unit (CPU)~~;

an interface disposed on a surface of the housing to enable communication between the device and the notebook computer when the device is docked;

a memory to store a second operating system, the second operating system is smaller in size and provides less functionality than the first operating system ; and

a central processing unit (CPU) to operate as a system processor of the notebook computer when the device is docked and to operate as a system CPU of the device when the device is undocked.

2. (Original) The electronic device of claim 1, further comprising an input controller to receive input data into the device when the device is undocked.

3. (Original) The electronic device of claim 2, further comprising core memory to store the input data when the device is undocked.

4. (Original) The electronic device of claim 3, further comprising an output controller to provide output data from the device when the device is undocked.

5. (Original) The electronic device of claim 4, further comprising a visual display disposed on a surface of the housing, the visual display being coupled to the input controller to provide the input data via pen-based entries on the display and being coupled to the output controller to provide the output data via the display.

6. (Cancelled).

7. (Previously Presented) The electronic device of claim 1, further comprising a battery to provide power to the CPU when the electronic device is undocked.

8. (Original) The electronic device of claim 7, wherein the interface is coupled to the battery to charge the battery when the electronic device is docked.

9. (Previously Presented) The electronic device of claim 8, wherein the notebook computer is to provide power to the CPU when the electronic device is docked.

10. (Previously Presented) The electronic device of claim 9, wherein the CPU is to operate at a higher frequency and at a higher voltage when the device is docked than when the device is undocked.

11. (Previously Presented) The electronic device of claim 1, wherein the CPU is to operate at a higher frequency and at a higher voltage when the device is docked than when the device is undocked.

12. (Currently Amended) A ~~base~~ notebook computer comprising:

a docking port to receive a hand-held core computer having a Central Processing Unit (CPU) to operate as a system CPU of the ~~base~~ notebook computer when the device is docked, ~~the base computer being exclusive of a CPU,~~ and to operate as a system CPU of the core computer when the ~~device~~ core computer is undocked, the hand-held core including a memory to store a first operating system;

an interface in the docking port to enable communication between the core computer and the ~~base~~ notebook computer when the core computer is docked; and

a memory to store a second operating system, the second operating system is larger in size and has greater functionality than the first operating system.

13. (Cancelled).

14. (Currently Amended) The ~~base~~ notebook computer of claim 12, wherein the interface is to couple a power supply of the base computer to a battery in the core computer to charge the battery and to provide power to the CPU when the core computer is docked.

15. (Currently Amended) The ~~base~~notebook computer of claim 14, wherein the CPU is to operate at a higher frequency and at a higher voltage when the CPU operates as a

system CPU of the ~~base~~notebook computer than when the CPU operates as a system CPU of the core computer.

16. (Currently Amended) The ~~base~~notebook computer of claim 12, wherein the CPU is to operate in one of a high power mode and a low power mode according to user preference.

17. (Currently Amended) A method of operating a computer system comprising:

operating a Central Processing Unit (CPU) as a system CPU of a notebook computer when a core computer is docked in a docking port of the notebook computer, the notebook computer including a memory to store a first operating system ~~and the notebook being exclusive of a CPU~~; and

operating the CPU as a system CPU of the core computer when the core computer is undocked, the core computer including a memory to store a second operating system , the second operating system is smaller in size and provides less functionality than the first operating system.

18. (Original) The method of claim 17, further comprising synchronizing memory of the notebook computer with memory of the core computer when the core computer is docked.

19. (Original) The method of claim 17, further comprising charging a battery in the core computer when the core computer is docked.

20. (Previously Presented) The method of claim 17, wherein operating the CPU as a system CPU of the notebook computer includes operating the CPU at a higher frequency and voltage than when operating the CPU as a system CPU of the core computer.

21. (Cancelled).